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S1	3	"6100833"	USPAT	OR	OFF	2004/12/07 15:25
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S3	3	"6100833" and sources	USPAT	WITH	OFF	2004/12/07 15:36
S4	0	"6100833" and resistors	USPAT	WITH	OFF	2004/12/07 15:36
S5	0	"6100833" and capacitors	USPAT	WITH	OFF	2004/12/07 15:37
S6	1	"6100833" and capacitor	USPAT	WITH	OFF	2004/12/07 16:12
S7	1	"6100833" and computer	USPAT	WITH	OFF	2004/12/07 16:13
S8	0	"6100833" and microprocessor	USPAT	WITH	OFF	2004/12/07 16:13
S9	2	"6100833" and controlled	USPAT	WITH	OFF	2004/12/07 16:15
S10	0	"6100833" and controller	USPAT	WITH	OFF	2004/12/07 16:15
S11	0	decoupling drive pickoff	USPAT	WITH	OFF	2004/12/21 10:24
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S13	3	decouple drive pickoff	USPAT	SAME	OFF	2004/12/21 10:26
S14	3	decouple drive pickoff	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/12/21 10:27
S15	6	decoupled drive pickoff	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/12/21 10:27
S16	3	decouple drive pickoff	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/12/21 10:27
S17	0	crosstalk drive pickoff	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/12/21 10:28
S18	204	cross drive pickoff	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/12/21 10:28

S19	4	drive pickoff attenuate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/12/21 10:29
S20	2	drive pickoff attenuates	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/12/21 10:29
S21	8	drive pickoff attenuated	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/12/21 10:53
S22	1430	((73/504.16,504.04,504.12,504.14, 862.59) or (310/309)).CCLS.	USPAT	OR	OFF	2004/12/21 10:54

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1 **Development of a CMOS integrated zero-crossing discriminator usin
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Jochmann, M.W.;
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Pages:308 - 311

[Abstract]   [PDF Full-Text (428 KB)]   **IEEE JNL**

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[Abstract]   [PDF Full-Text (1363 KB)]   **IEEE CNF**

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